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TRANSMITTAL			Application / Conf. No.	10/082,441 / 6299
			Filing Date	February 22, 2002
FORM			First Named Inventor	L. James Hwang
(to be used for all correspondence after initial filing)			Examiner Name	Stacy Whitmore
Mail Stop: AMENDMENT			Art Unit	2825
Express Mail			Patent No.	
Receipt No. Total Number of Pag	es in This Submission		Attorney Docket Number	X-1000 US
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT				
Firm or Customer Number			24309	D 11 1 27 (72
Attn:	Kim Kanzaki		(Customer Number)	Reg. Number 37,652
Signature				
Date	July 21, 2005/		Charge any additiona to our Deposit Accour	fees required/credit any overpayment t Number: 24-0040
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X-1000 US 0/082,441

IN THE UNITED STATES PATENT OFFICE

pplicants:

L. James Hwang et al.

Assignee:

Xilinx, Inc.

Title:

"Method and System for Resource Allocation in FPGA-Based

System-On-Chip (SoC)"

Serial No.:

10/082,441

Filed:

02-22-02

PATENT

Conf. No.: 6299

Examiner:

Stacy Whitmore

Art Unit:

2825

Docket No.:

X-1000 US

Conf. No.:

6299

Mail Stop AMENDMENT COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

In response to the Restriction Requirement mailed from the Patent Office on June 29, 2005, Applicants elect with traverse to prosecute Group I: Claims 1-27. Applicants hereby cancel Claims 28-33 (Group II) but reserve the right to prosecute Group II (Claims 28-33) in a subsequent divisional application.

A listing of the claims in the present application is appended hereto.

Respectfully submitted,

Kim/Kanzaki

Attorney for Applicants

Reg. No. 37,652

I hereby certify that this correspondence is being deposited with the United States Postal Service as **first class mail** in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on July 21, 2005.

Pat Slaback

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